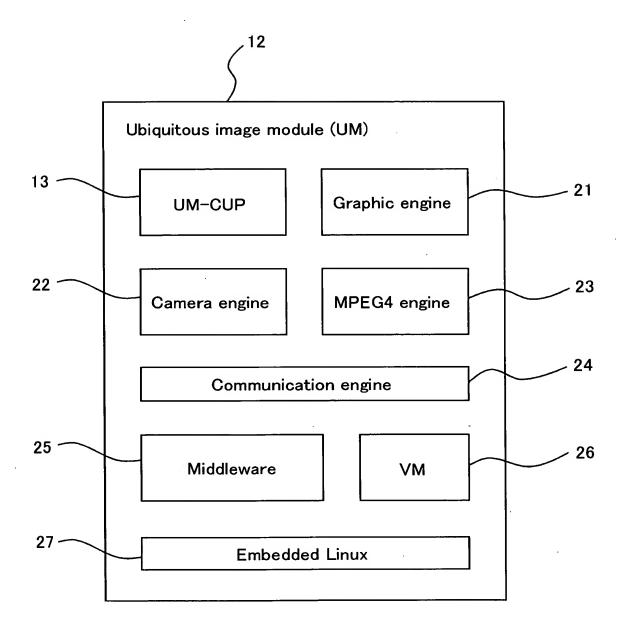
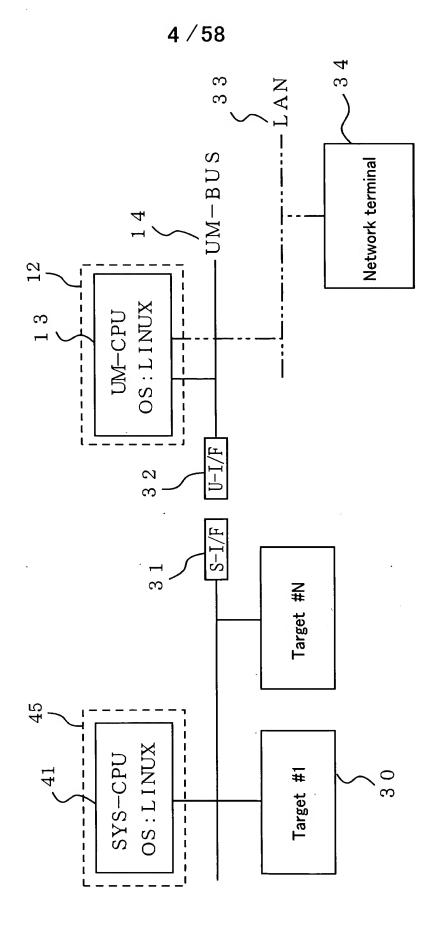
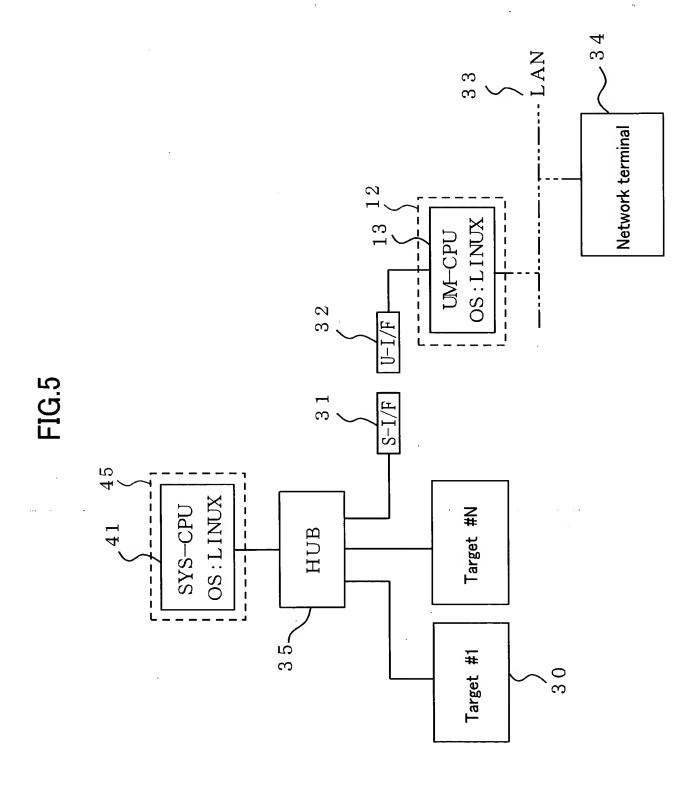
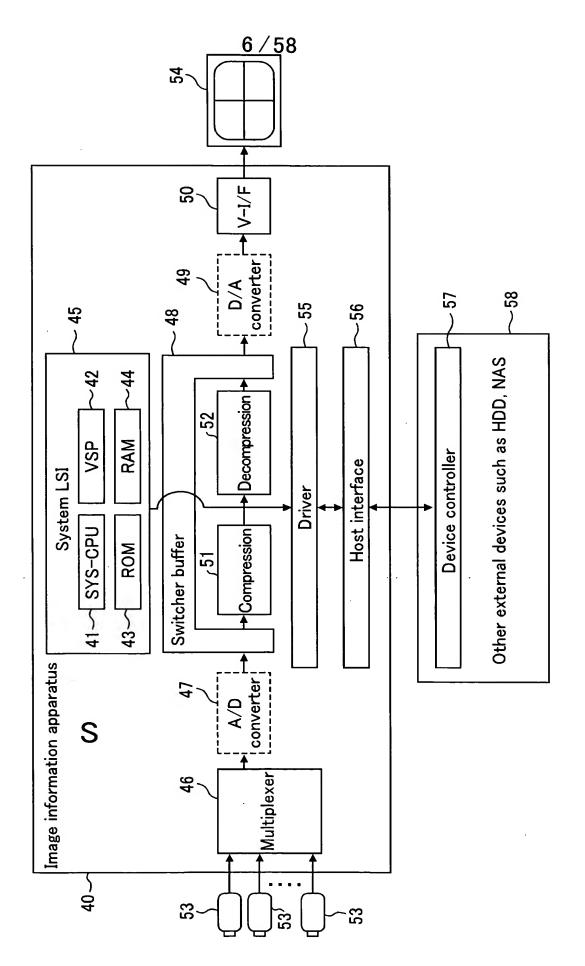


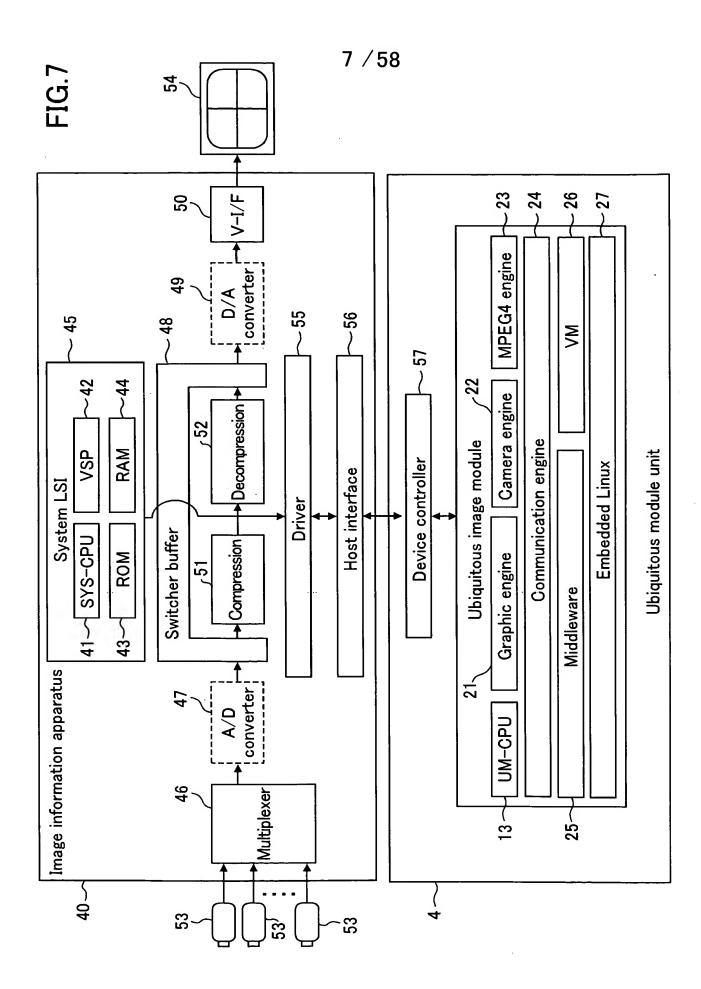
FIG.3











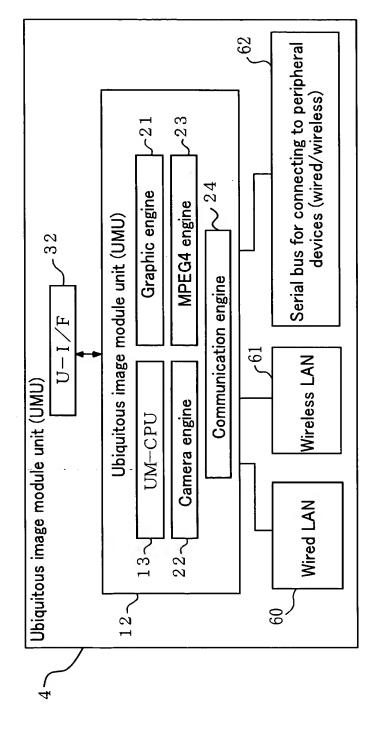


FIG.8

FIG.9

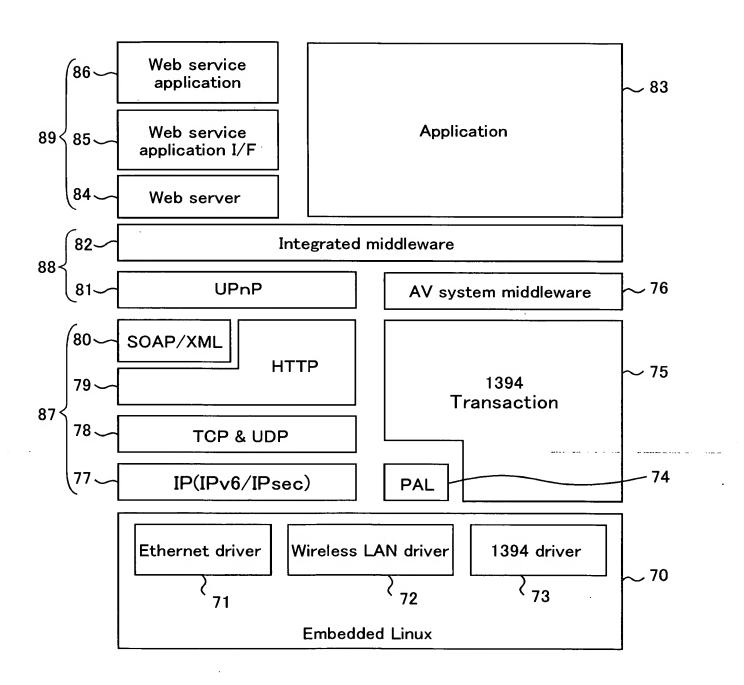


FIG.10

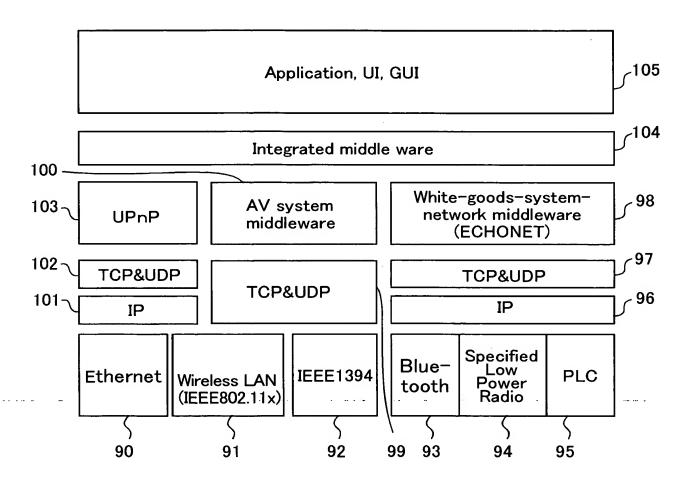


FIG.11

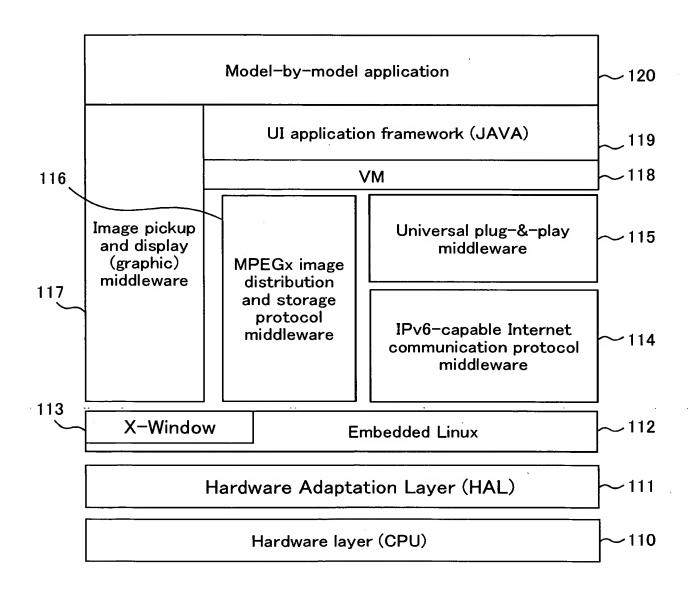
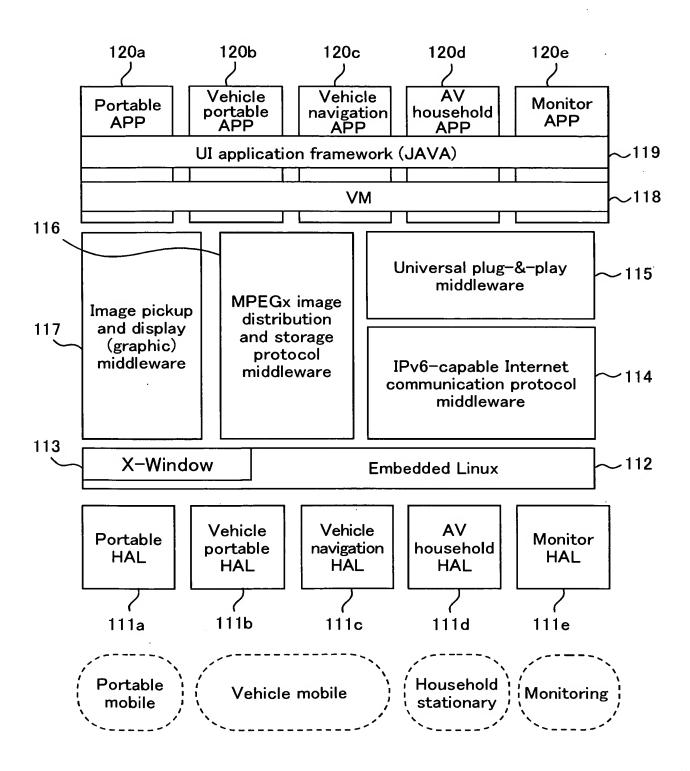


FIG.12



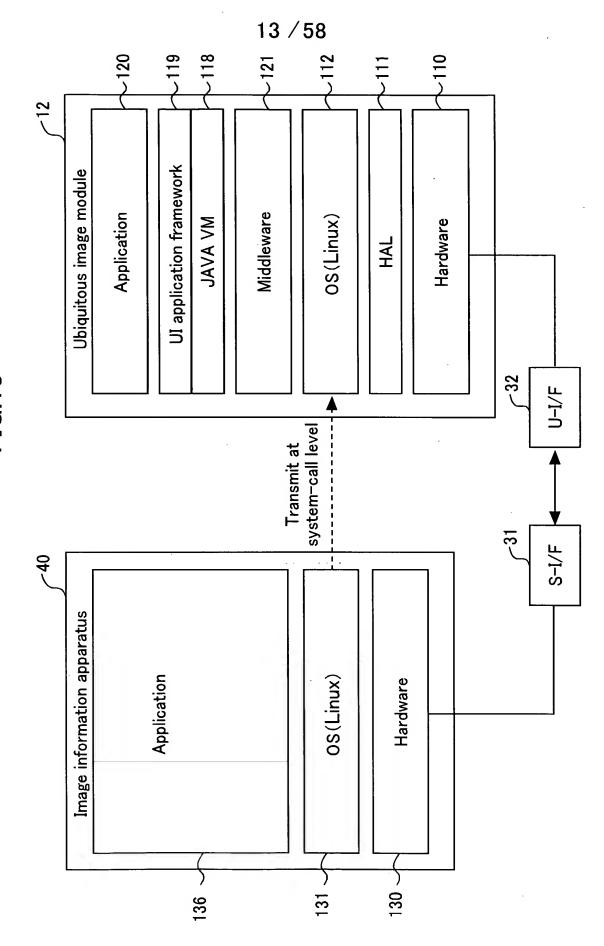


FIG.13

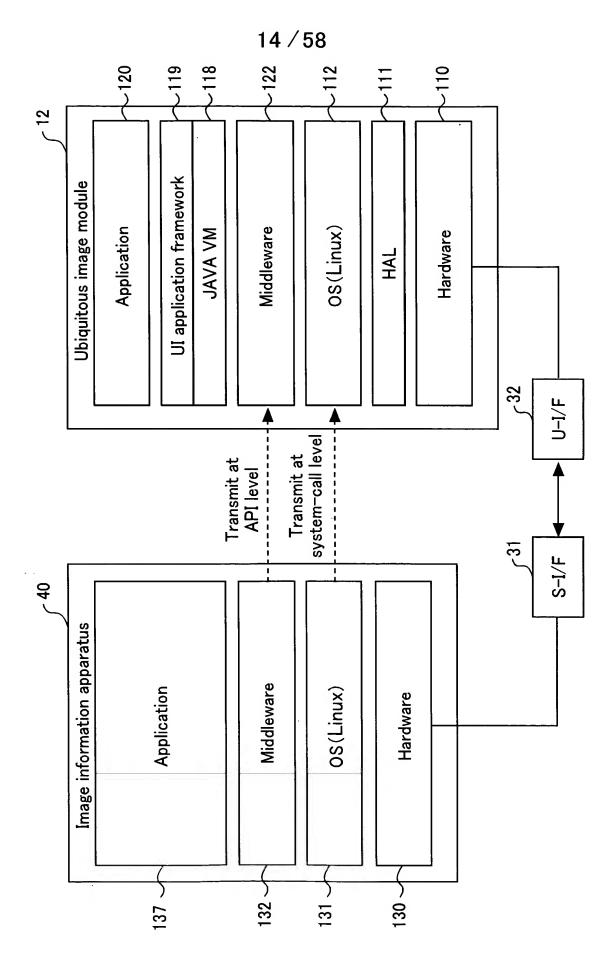
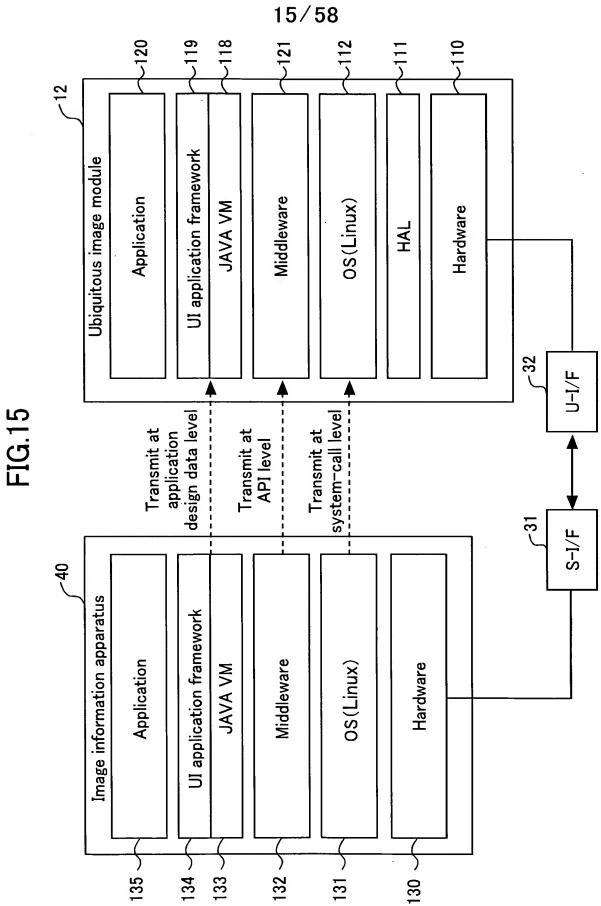
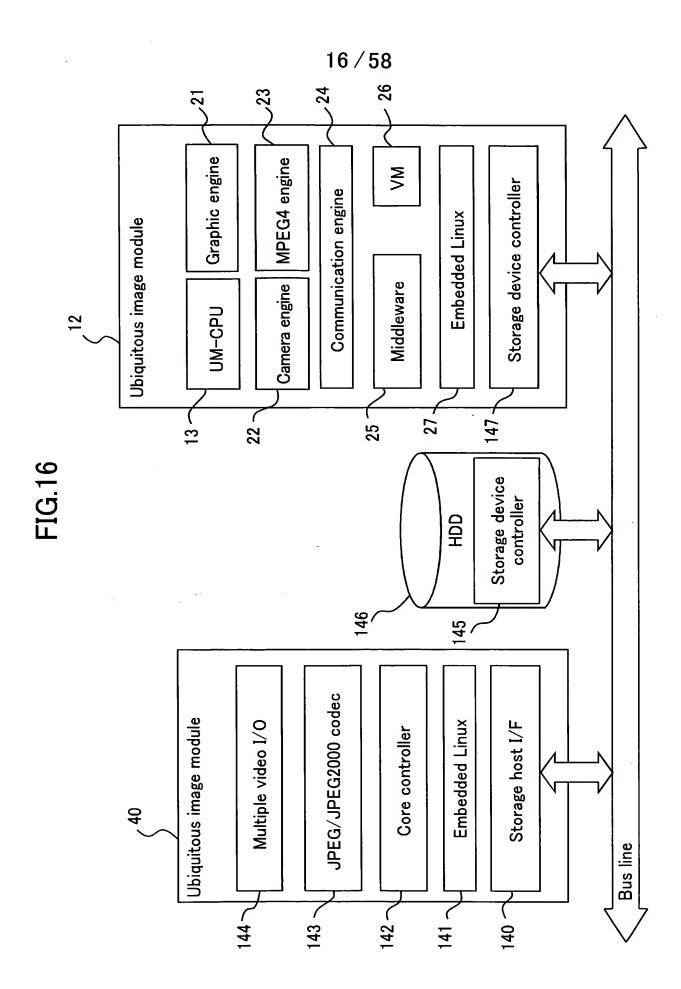
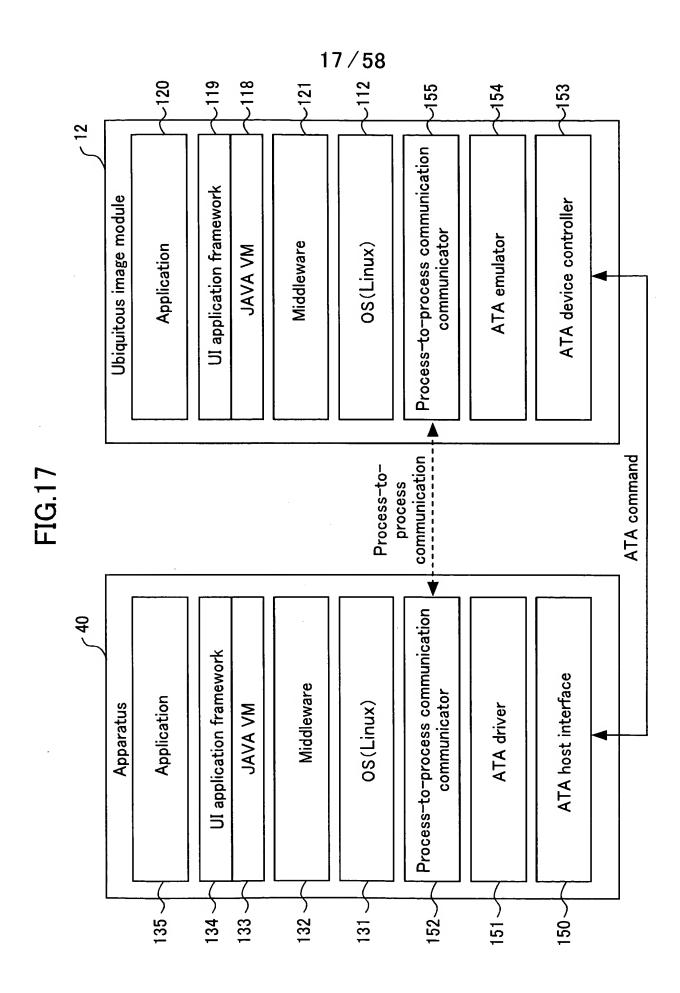


FIG.14







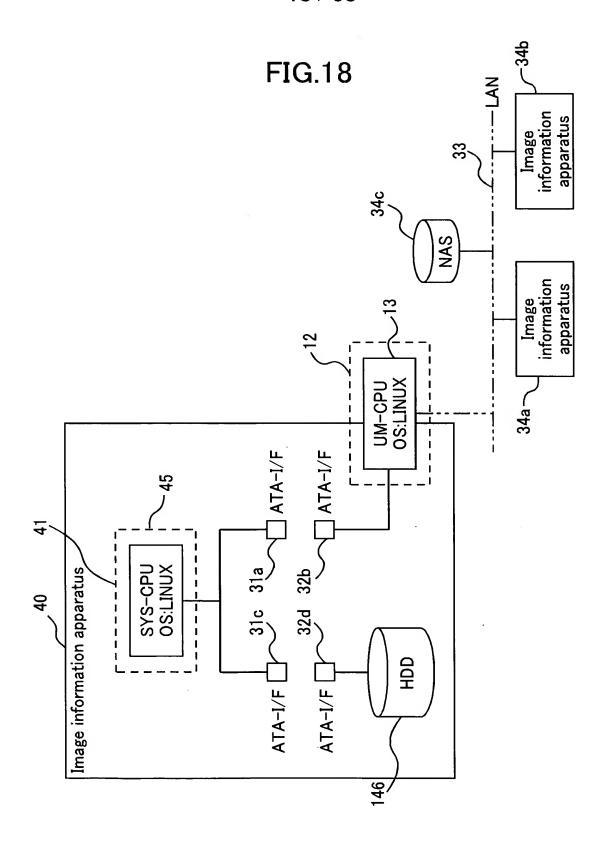


FIG.19

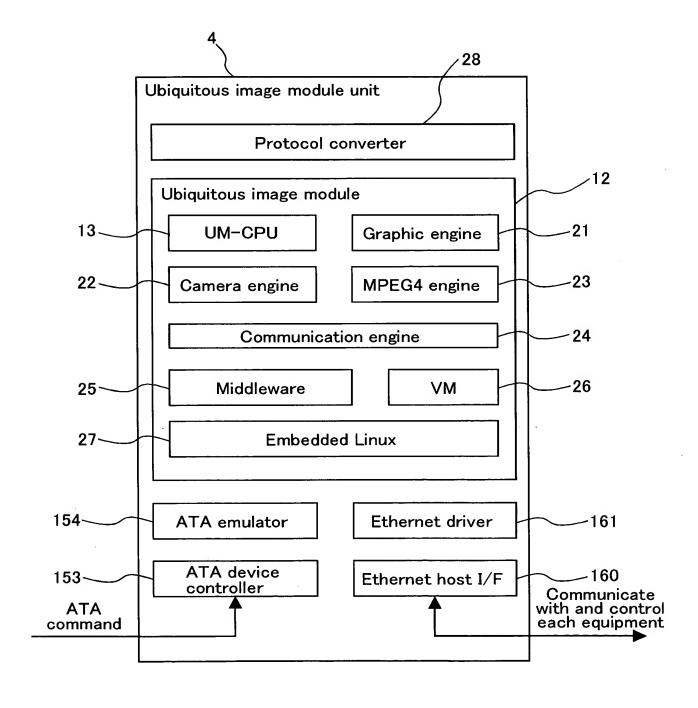


FIG.20

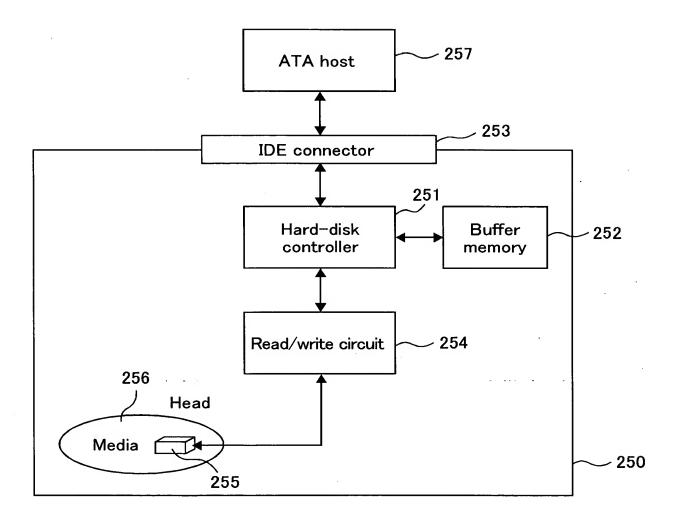


FIG.21

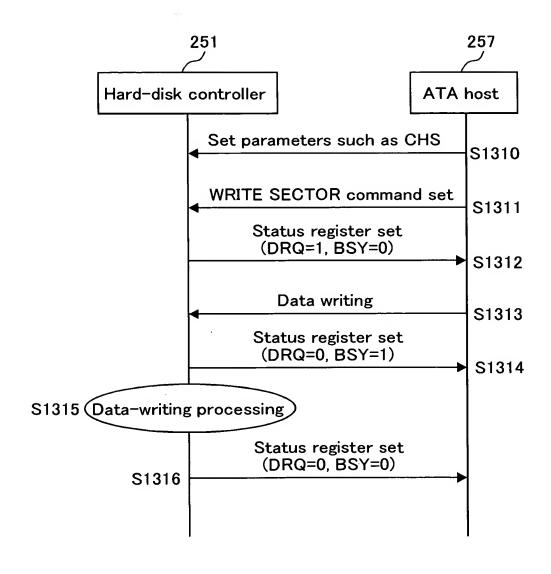


FIG.22

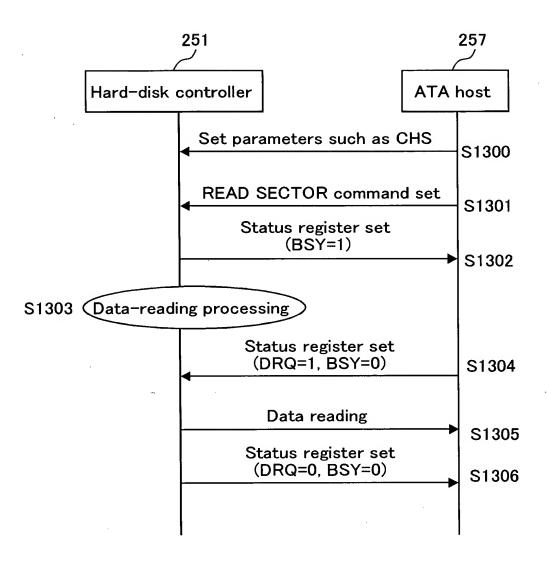


FIG.23

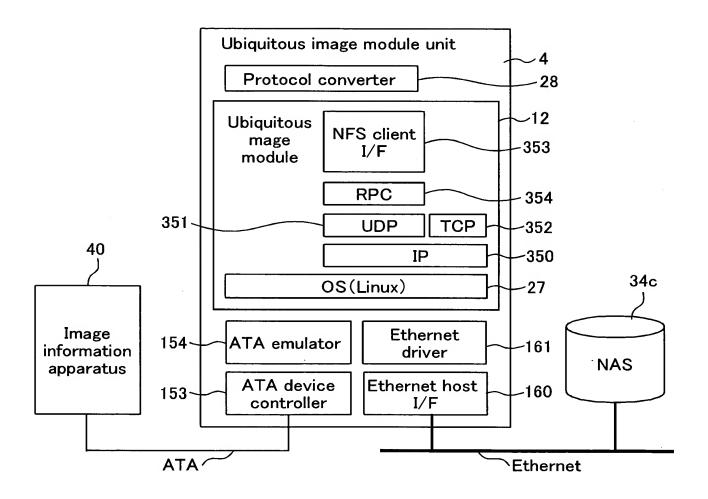
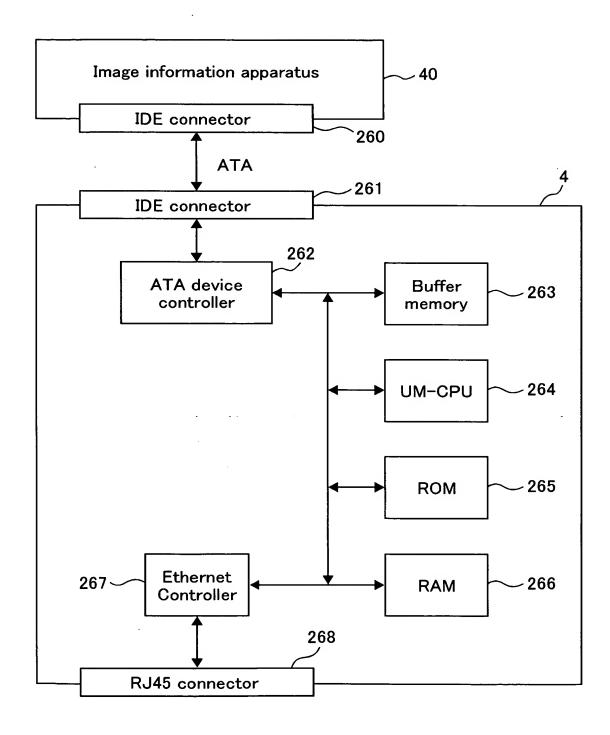


FIG.24



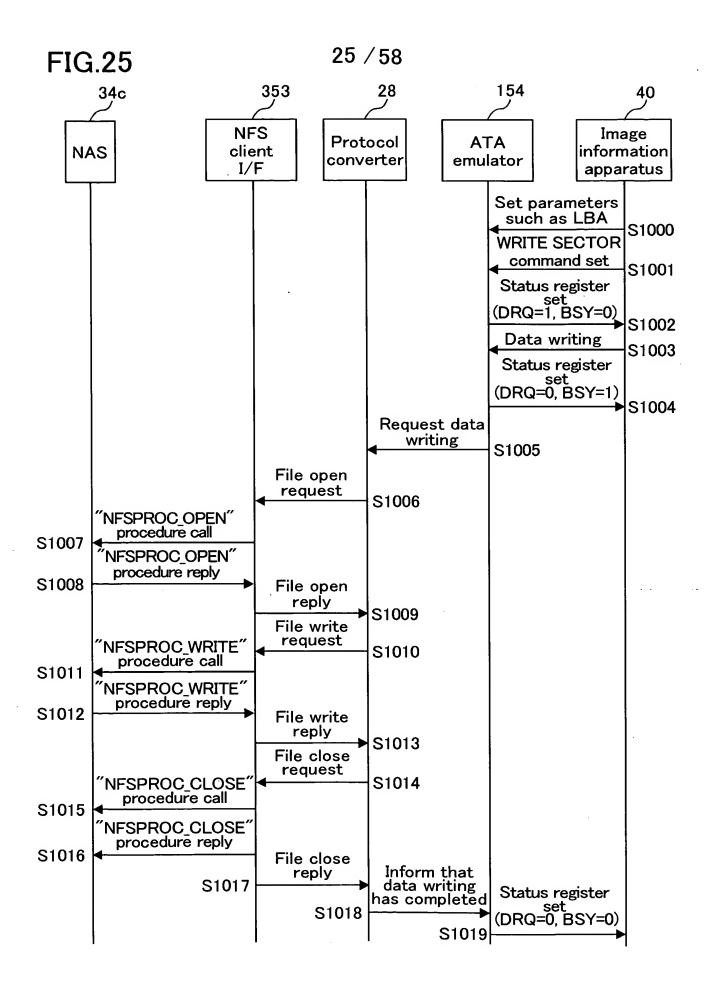
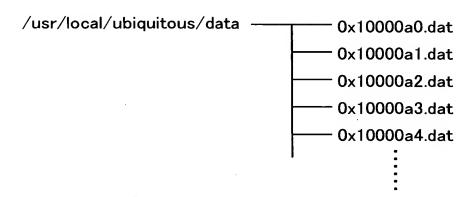
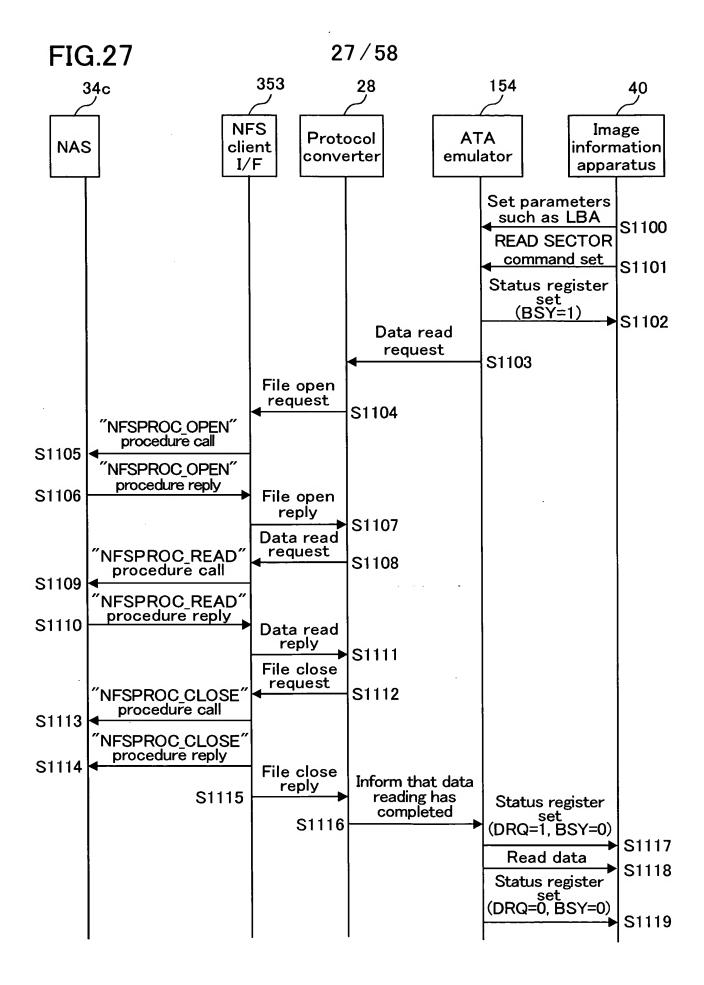
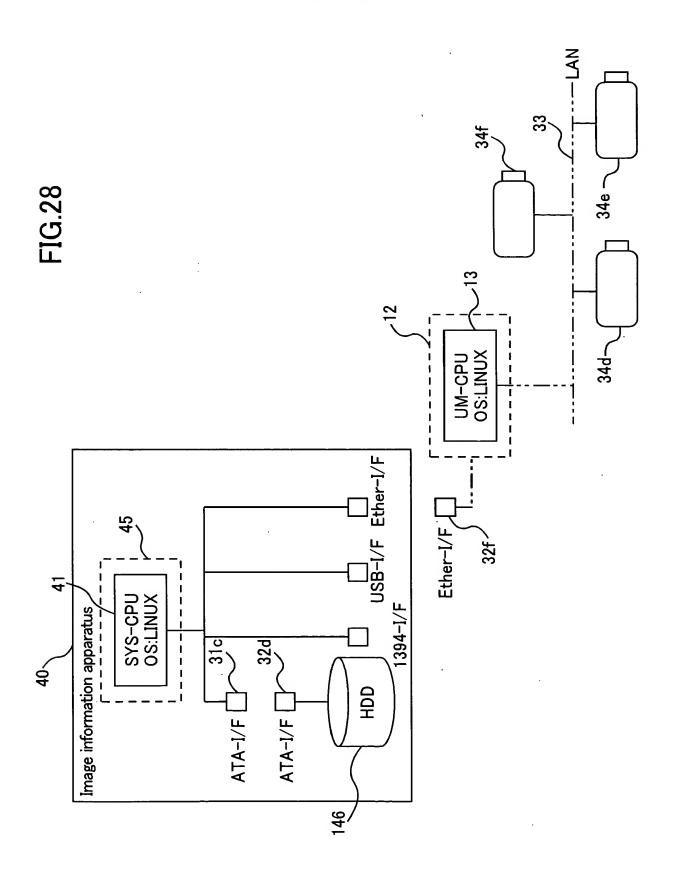


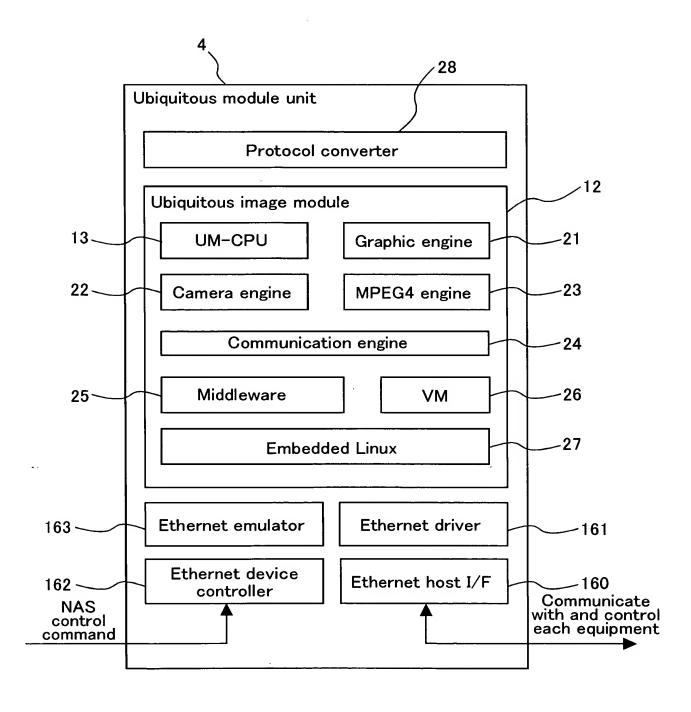
FIG.26

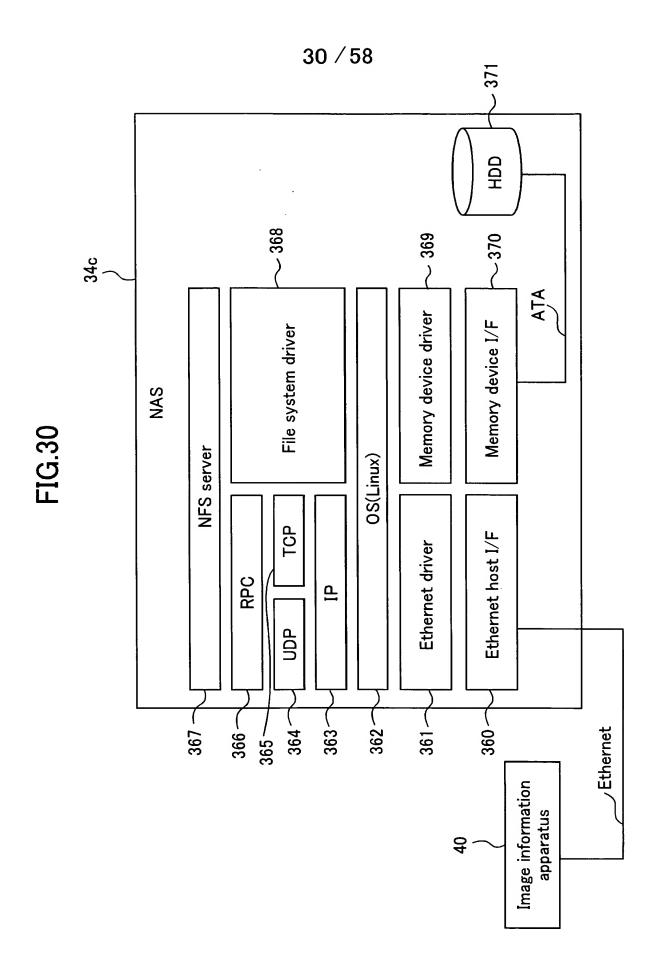


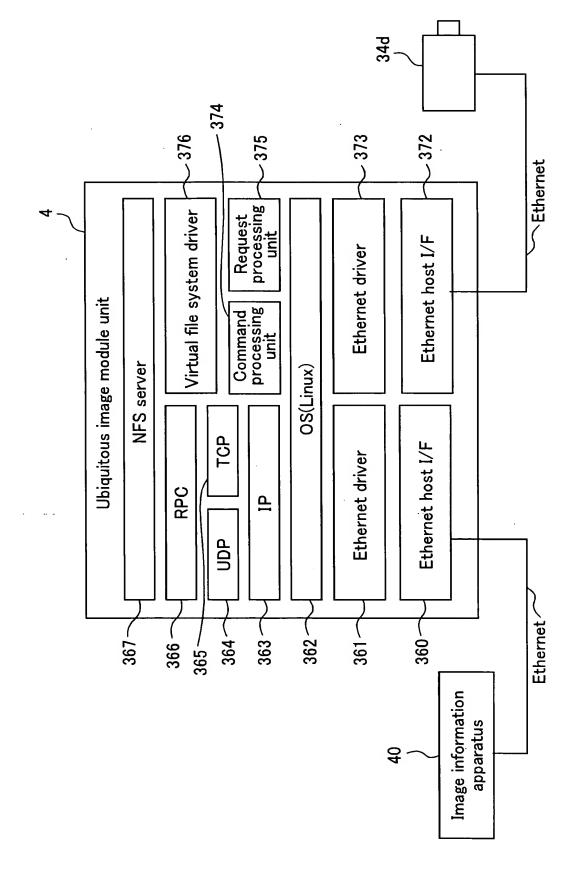




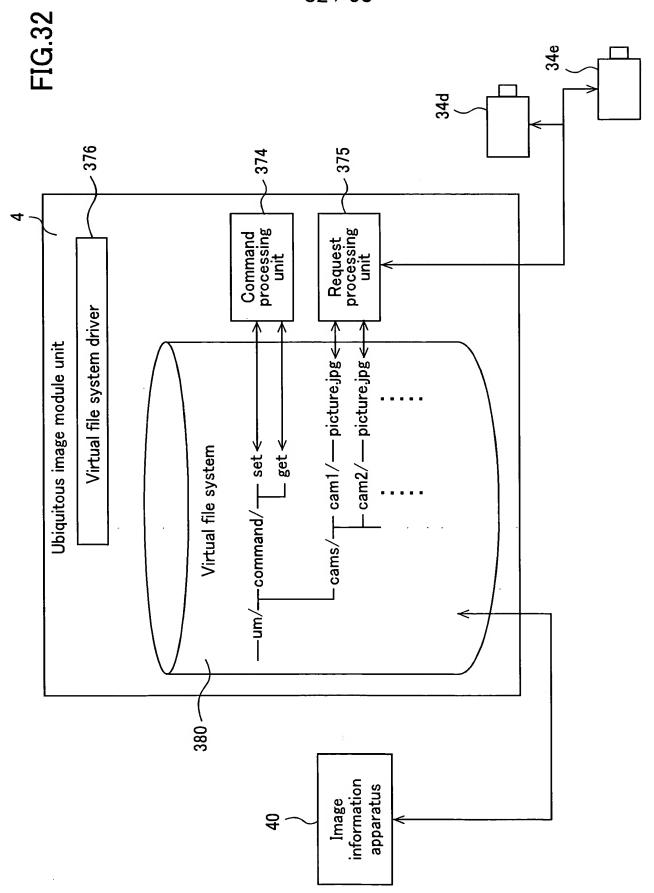
29 / 58 FIG.29







-IG.3



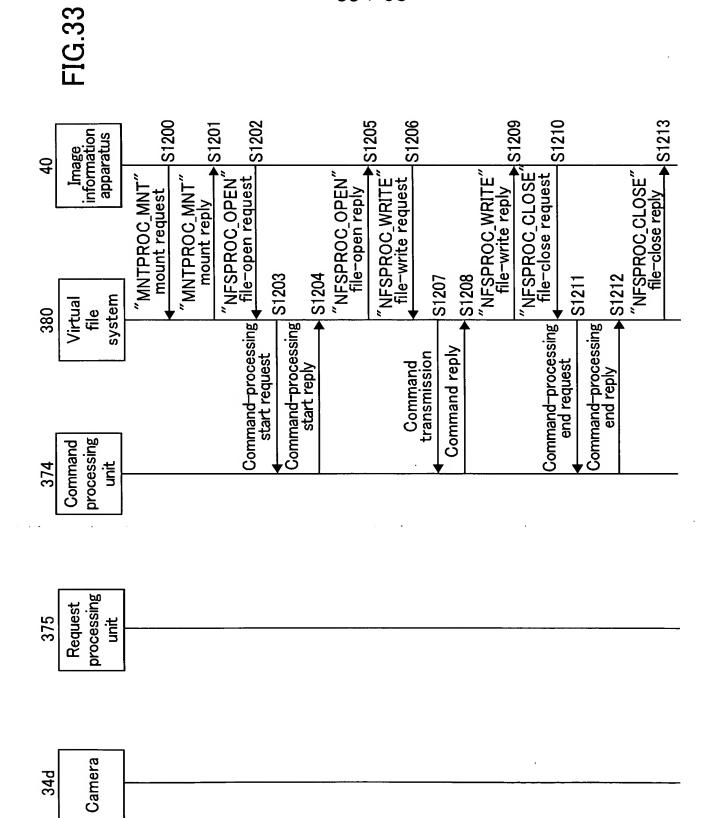


FIG.35

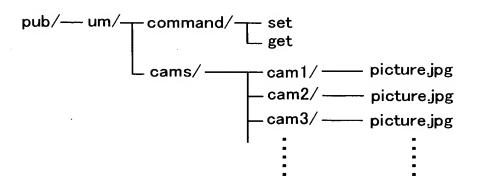


FIG.36

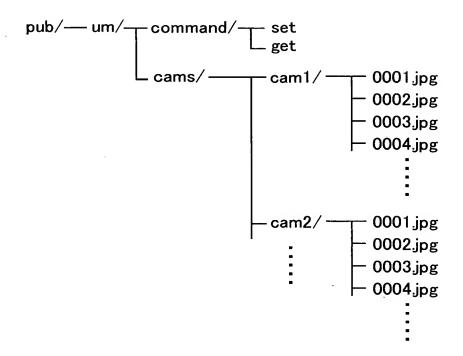
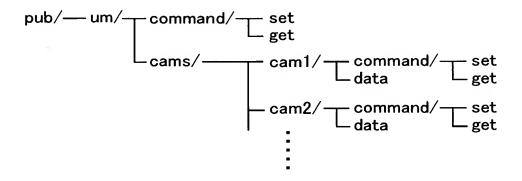
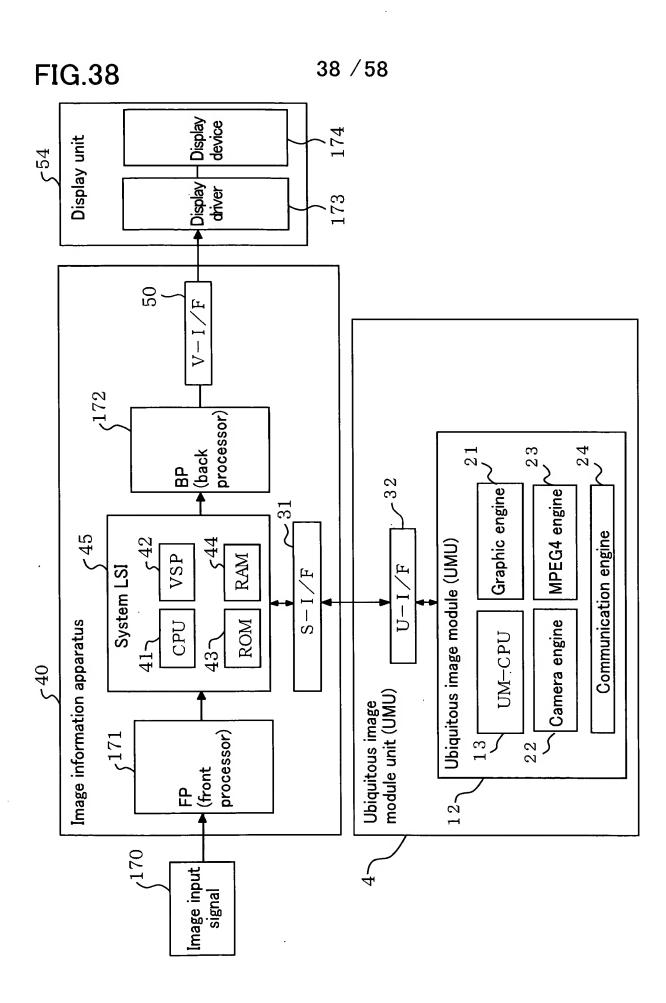
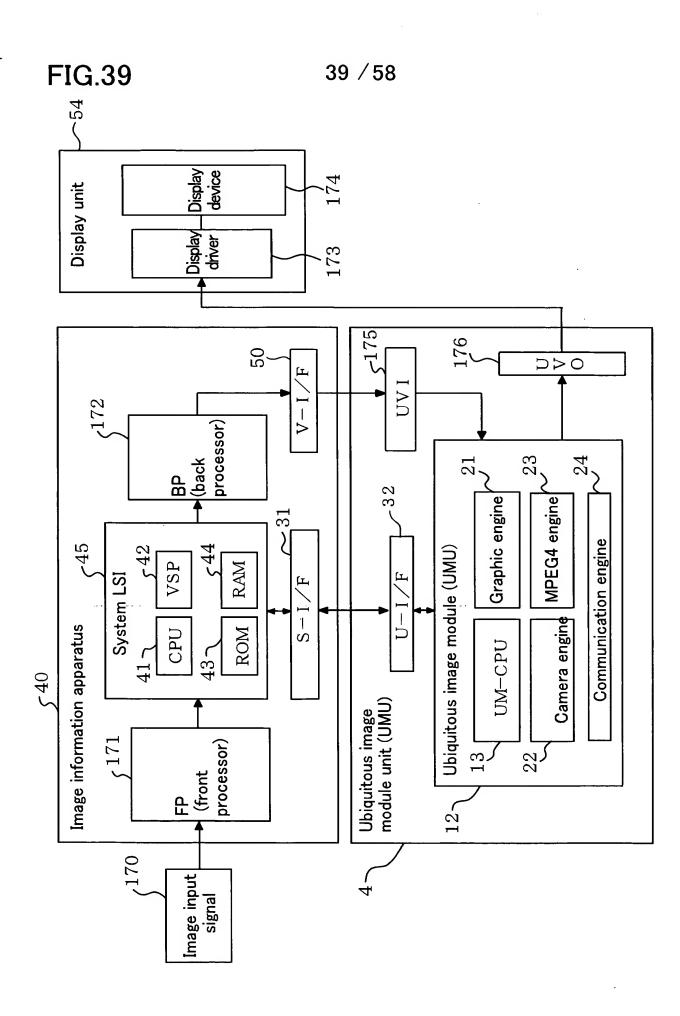


FIG.37







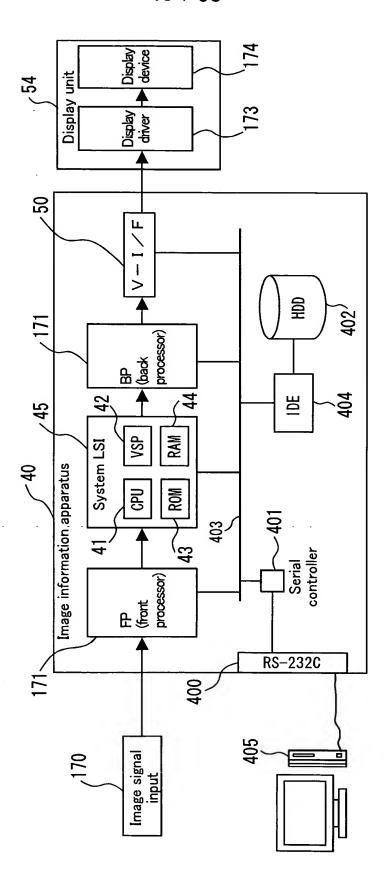
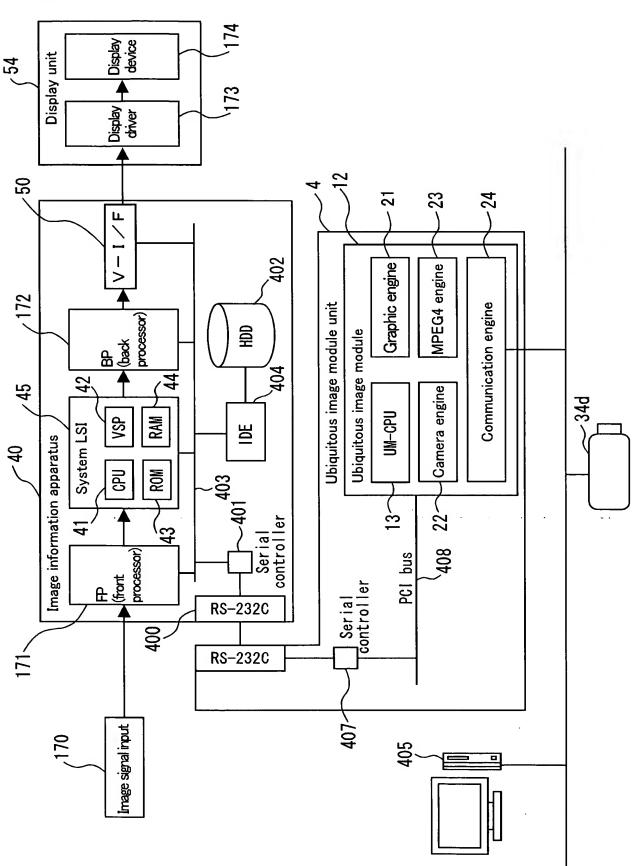
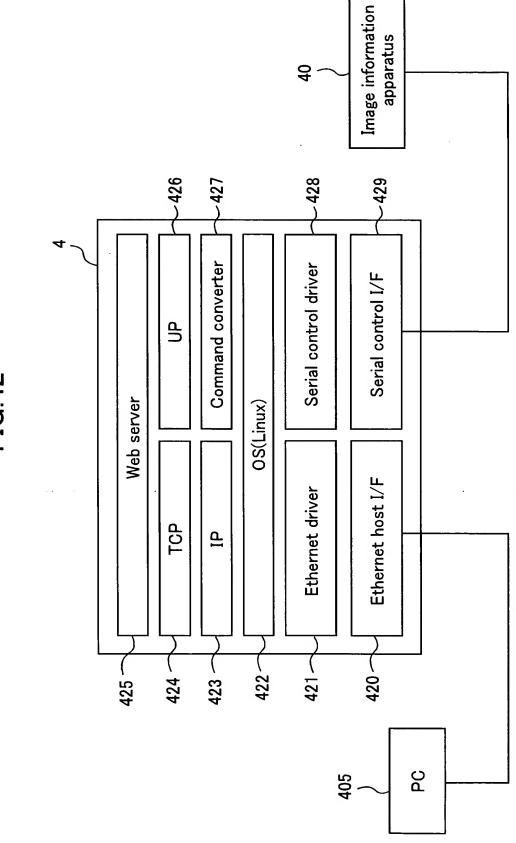
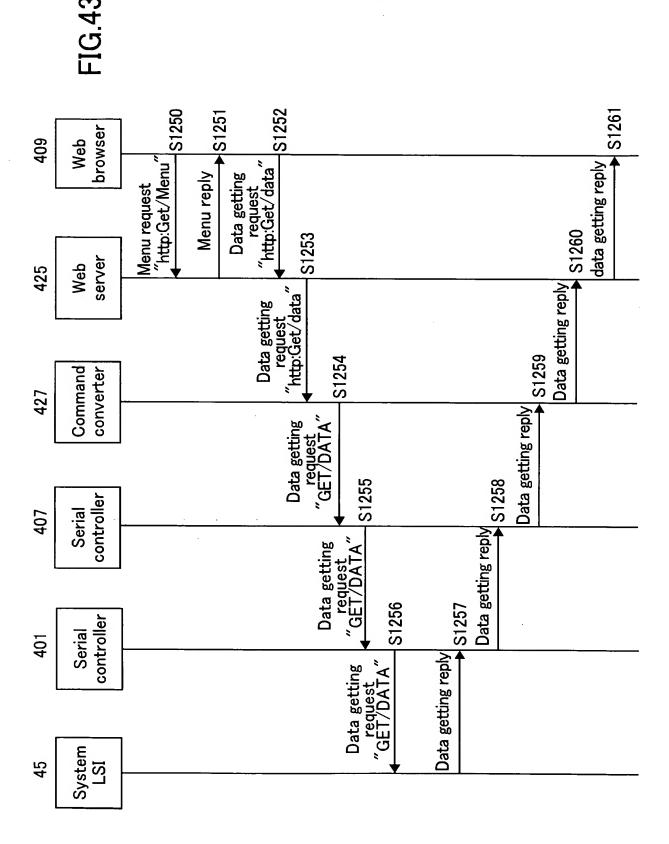


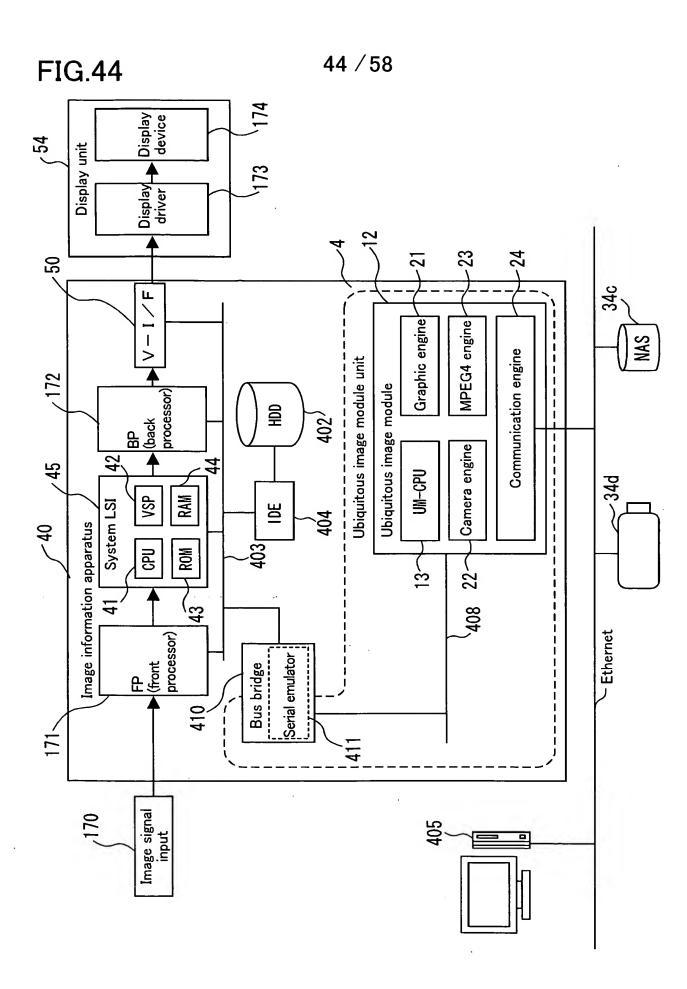
FIG.40



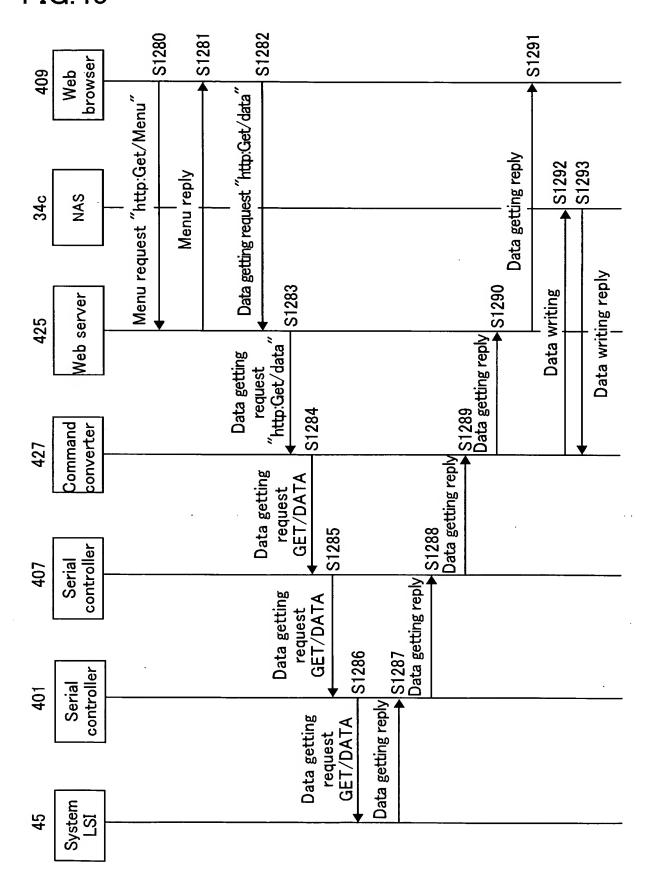


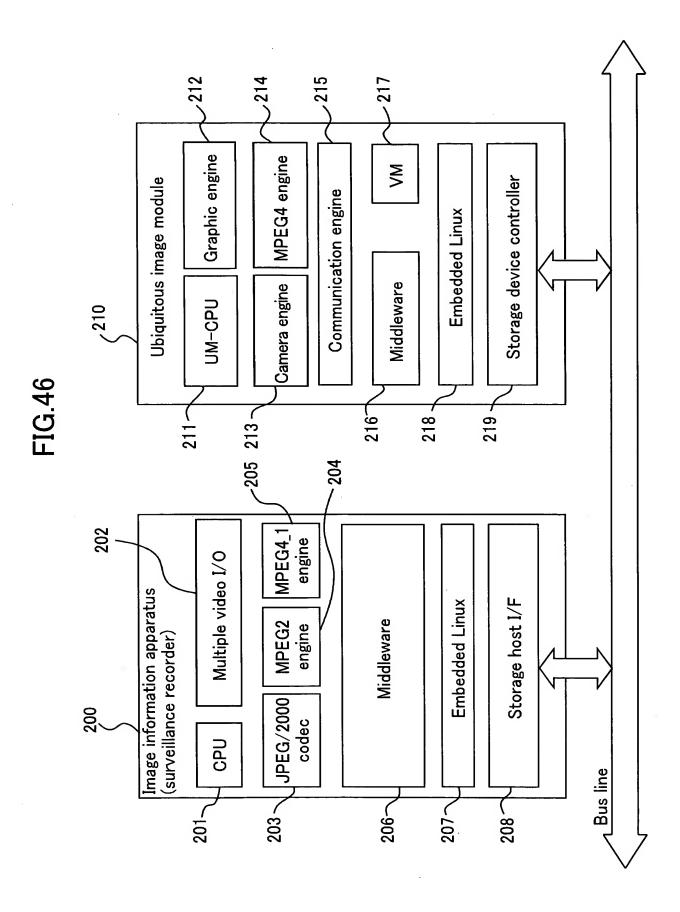
-1G 42





45 / 58





47 / 58

FIG.47

48 /58 FIG.48

Equipment setting	230a
Network setting	230b
Cooperation information	230c

FIG.49

Administration No	Hardware engine	231
r_1	Multiple video I/O	
r_2	JPEG/2000 codec	
r_3	MPEG2 engine	
r_4	MPEG4_1 engine	
i i	•	

50 / 58

FIG.50

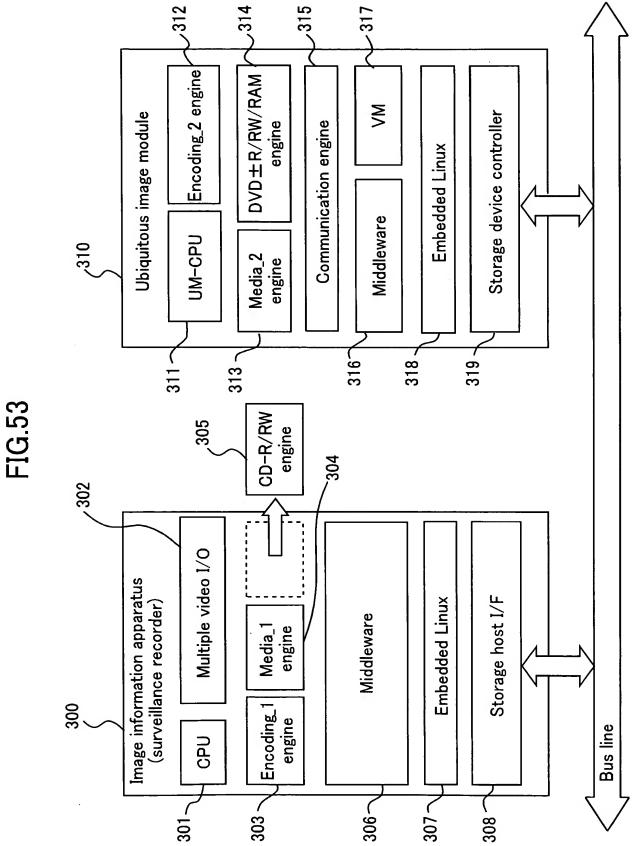
Administration No	Hardware engine	232
u_1	Graphic engine	
u_2	Camera engine	
u_3	MPEG4_2 engine	
:	:	

FIG.51

233

No.	Hardware engine	Administration No	Controllable flag	Control flag	Access flag
1	Multiple video I/O	r_1	F	F	F
2	Graphic engine	u_1	F	F	
3	JPEG/2000 codec	r_2	F	F	F
4	Camera engine	u_2	F	F	
5	MPEG2 engine	r_3	F	F	F
6	6 MPEG4 engine	r_4	F		
O		u_3	F	F	
:	:	:	•	-	:

FIG.52



	Controllable flag	IL.	ч	ட	:	•••
331b \	Hardware engine	Multiple video I/O	Endoding_1 engine	Media_1 engine	CD-R/RW engine	•••
	Administration No	r_1	r_2	r_3	r_4	•
			<u>〔</u>	7		
	Controllable flag	Ł	J	J	Ŧ	•••
331a \	Hardware engine	Multiple video I/O	Endoding_1 engine	Media_1 engine	CD-R/RW engine	•••
	Administration No	r_1	r_2	r_3	r_4	

-IG.54

	Controllable flag	L	ъ	Н.	•••
332b \	Administration Hardware engine No	Endoding_2 engine	Media_2 engine	DVD±R/RW/RAM engine	
	Administration No	u_1	7 ⁻ n	£ ⁻ n	•••
			1	Ź	
į	Controllable flag	ш	ш	ъ.	•••
332a \	Hardware engine	Endoding_2 engine	Media_2 engine	DVD±R/RW/RAM engine	•••
	Administration No	u_1	n_2	n_3	• • •

FIG.55

FIG.56

r	•	•
L)
C		5
		-
_	-	_

	Access flag	ட							•••
	Control flag	L	ъ		ш			Ł	•••
333	Controllable flag	L.	Ь	Ŧ	ட	F	٠	£	•••
	Administration No flag	r_1	u_1	r_2	7 ⁻ n	r_3	1-4	£_u	
	Hardware engine	Multiple video I/O	Endoding_2 engine	Endoding_1 engine	Media_2 engine	Media_1 engine	CD-R/RW engine	DVD±R/RW/RAM engine	
	Š.		2	က	4	2	9	7	

